

BiM3C

Issue 2, 14 May 2021

869MHz Wide Band FM radio transceiver

The BiM3C radio transceiver module is an enhanced high power replacement for the European version of BiM3A.

Two frequency variants are available in the European unlicensed band, one with 5mW RF power with no duty cycle restriction other with 25mW RF power with 1% duty cycle restriction.

Features

Figure 1: BiM3C-868.

- Complies with European harmonised standards EN 300 220 and EN 301 489
- Complies with US FCC Part 15.249 and Canadian IC RSS-210
- RF power output: +14dBm (25mW) on 868.30MHz (EU) with <1% duty cycle restriction
+7dBm (5mW) on 869.85MHz (EU) with no duty cycle restriction
- Data rates: up to 10kbps or 64kbps
- Crystal controlled PLL FM circuitry for both Tx and Rx
- SAW front end filter and full screening
- Receiver Sensitivity: -102dBm for 1ppm BER (64kbps) -107dBm for 1ppm BER (10kbps)
- Supply: 2.9V - 16V 20mA TX (5mW), 14mA Rx
- Usable range up to 300 metres external, 75 metres in building
- Received Signal Strength Indicator (RSSI)
- Analogue and digital baseband
- AFC (automatic frequency control)
- Low profile with small footprint
- 33 x 23 x 5mm

Applications

- PDAs, organisers and laptops
- Handheld terminals
- EPOS equipment, barcode scanners, belt clip printers
- Data loggers
- Audience response systems
- In-building environmental monitoring and control
- Security and fire alarms
- Restaurant ordering systems
- Vehicle data up/download

Evaluation platforms: Uni-Eval, NBEK + BiM / SMX carrier

Functional overview

The transmit section of the BIM3C consists of a frequency modulated crystal locked PLL feeding a buffer amplifier and RF filter. A Tx select line controls operation. The transmitter achieves full RF output typically within 1ms of this line being pulled low. Modulation is applied at the TXD input and may be either a serial digital stream toggling between 0V and 3V (digital drive) or a high level analogue waveform with the same peak limits (linear drive). Modulation shaping is performed internally by a 2nd order lowpass filter, which minimises spectral spreading. The RF output is filtered to ensure compliance with the appropriate regulations and fed via a Tx/Rx changeover switch to the antenna pin.

The receive section is a single conversion FM superhet with an IF of 10.7MHz. A SAW bandpass filter in the receiver front-end provides image rejection and suppression of other unwanted out-of-band signals. Like the transmitter, the receiver is controlled by its own active low RX select line. A post-detection lowpass filter establishes the signal bandwidth and ensures clean operation of the subsequent adaptive data slicer. The slicer is optimised for balanced data such as bi-phase code. A received signal strength (RSSI) output with 60dB of range is provided.

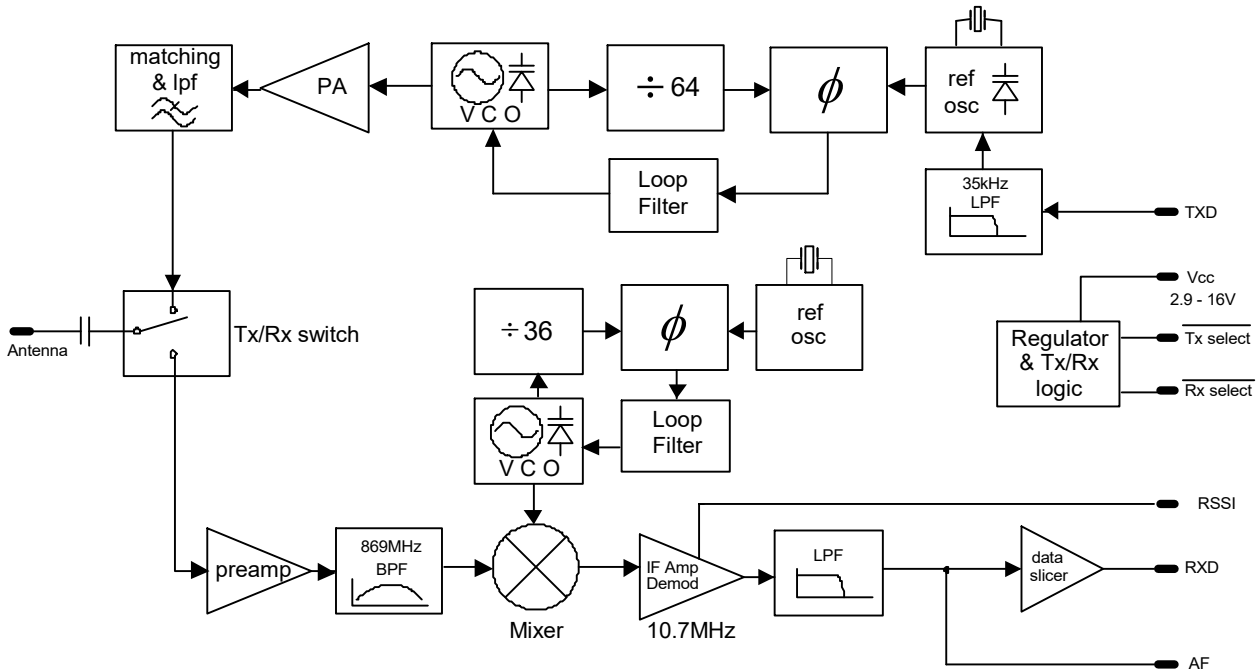


Fig. 2: Block diagram

Pin description

RF GND *pins 1 & 3*

RF ground pins, internally connected to the module screen and to pins 5, 9, 10 & 18 (0V). These pins should be connected directly to the RF return path (e.g. coax braid, main PCB ground plane etc).

Antenna *pin 2*

50Ω RF connection to the antenna, DC-isolated. See pages 7 & 8 for details of suitable antennas and feeds.

0V (GND) *pins 5, 9, 10 & 18*

Supply ground connection and screen.

RSSI *pin 11*

Received Signal Strength Indicator with 60dB range, operational when Rx is enabled. Output voltage nominally 0.3 - 0.50Vdc (no signal), 1.50Vdc (maximum). See page.6 for typical characteristics.

RXD *pin 12*

Digital output from internal data slicer. The output is a squared version of the signal on pin 13 (AF) and may be used to drive a decoder directly. The data is true data, i.e. as fed to the transmitter. Output is "open-collector" format with internal 10kΩ pullup to Vcc (pin 17).

AF *pin 13*

Buffered & filtered analogue output from FM demodulator. Standing DC bias of 1V approx. Useful for test purposes or for driving external decoders. External load should be >10kΩ // <100pF

TXD *pin 14*

This DC-coupled transmitter modulation input will accept either serial digital data or high level linear signals. Drive signal must be limited to 0V min, 3V max. See page 6 for suggested drive methods. Input impedance >50kΩ.

TX select *pin 15*

Active-low Transmit select. 47kΩ internal pull-up to Vcc.

RX select *pin 16*

Active-low Receive select. 47kΩ internal pull-up to Vcc.

Pin 15 TX	Pin 16 RX	Function
1	1	Power down (<0.16mA)
1	0	Receiver enabled
0	1	Transmitter enabled
0	0	Self test loop-back*

* Loop-back allows the receiver to monitor the transmitted signal. Under these conditions the Tx radiated signal level will be reduced to approximately -10dBm).

Vcc *pin 17*

DC +ve supply pin. +2.9 to +16.0 volts @ <17mA (@ 5mW). The supply should be clean, <20mV_{P-P} ripple.

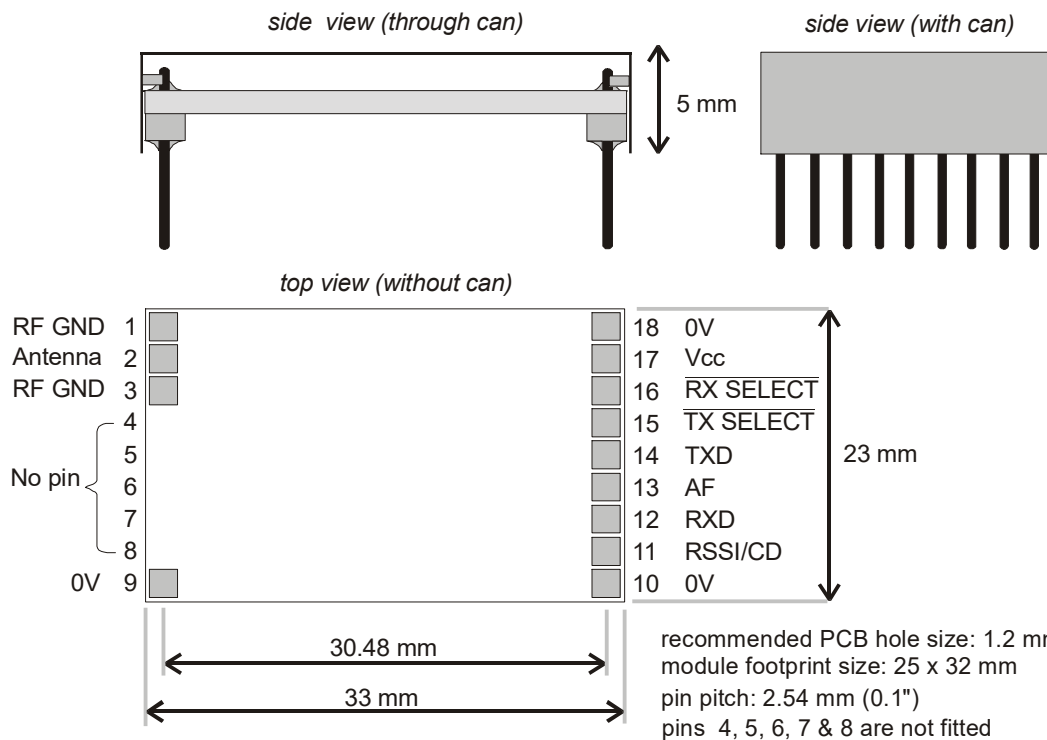


Fig. 3: Physical dimensions

Weight: 7g (typ.)

Absolute maximum ratings

Exceeding the values below may cause permanent damage to the module.

Operating temperature	-20°C to +70°C
Storage temperature	-40°C to +100°C
Vcc (pin 17)	-0.3V to +16V
TX, RX select (pins 15, 16)	-9V to +16V
All other pins	-0.3V to +Vcc
Antenna (pin 2)	±50V DC, +10dBm RF

Performance specifications

Figures apply to standard version @ Vcc=3.0V, temperature +20°C, unless stated.

General	pin	min.	typ.	max.	units	notes
Supply voltage	17	2.9	3.0	16	V	
Tx supply current (869.85)	17	-	20	-	mA	
Tx supply current (868.3)	17	-	37	-	mA	
Rx supply current	17	10	14	17	mA	Note 1
RF centre frequency	-	-	869.85	-	MHz	
RF centre frequency	-	-	868.30	-	MHz	
Antenna port impedance	2	-	50	-	Ω	Tx or Rx
TX & RX select: high (<i>deselect</i>)	15, 16	Vcc-0.5		Vcc	V	
low (<i>select</i>)	15, 16	0		0.5	V	
Internal select pull-ups	15,16	-	10	-	kΩ	To Vcc (pin 17)
Balanced code bit rate	12	-	-	64	kbps	-64 version
Balanced code bit rate	12	-	-	10	kbps	-10 version

Transmitter section	pin	min.	typ.	max.	units	notes
RF power output @ 869.85MHz	2	-	+7	-	dBm	100% duty cycle
RF power output @ 868.30MHz	2	+13	+14	+15	dBm	1% duty cycle
TX harmonics/spurious emission	2	-	-55	-40	dBm	
Initial centre frequency accuracy	-	-10	0	+10	kHz	
FM deviation	-	±20	±27	±35	kHz	Peak
Modulation bandwidth	-	0	-	35	kHz	@ -3db
Modulation distortion	-	-	5	10	%	Note 2
TX spectral bandwidth @-40dBc	2	-	-	250	kHz	worst case
TXD input level: high	14	2.8	-	3.0	V	Note 3
low	14	0	-	0.2	V	Note 3
TX power up to full RF	2	-	1	1.5	ms	Note 4

Receiver section	pin	min.	typ.	max.	units	notes
RF sensitivity, 10dB S/N	2, 13	-	-109	-	dBm	-64 version
RF sensitivity, 1ppm BER	2, 12	-	-102	-	dBm	-64 version
RF sensitivity, 10dB S/N	2, 13	-	-109	-	dBm	-10 version
RF sensitivity, 1ppm BER	2, 12	-	-107	-	dBm	-10 version
RSSI output, no signal	11	0.3	0.37	0.5	V	
RSSI output, max indication	11	-	1.5	1.6	V	-50dBm RF input
RSSI range	2, 11	-	60	-	dB	
IF bandwidth	-	-	180	-	kHz	
Image rejection (f _{RF} -21.4MHz)	2	40	50	-	dB	
IF rejection (10.7MHz)	2	100	-	-	dB	
Local osc. leakage, conducted	2	-	-117	-110	dBm	
Baseband bandwidth @ -3dB	13	0	-	50	kHz	-64 version
Baseband bandwidth @ -3dB	13	0	-	7.8	kHz	-10 version
AF output signal level	13	200	330	400	mV p-p	Note 5
DC offset on AF output	13	0.8	0.85	0.9	V	Note 6
Distortion on recovered AF	13	-	1	10	%	Note 7
Ultimate (S+N)/N	13	-	40	-	dB	-50dBm RF input
Load capacitance, AF & RXD	12, 13	-	-	100	pF	

Receiver section	pin	min.	typ.	max.	units	notes
Dynamic Timing						
<i>Rx power up with signal present</i>						
Power up to valid RSSI, $t_{PU-RSSI}$	11	-	1	-	ms	
Power up to stable data, $t_{PU-data}$	12	-	1.3	1.5	ms	-64 version (Note 9)
Power up to stable data, $t_{PU-data}$	12	-	1.6	2	ms	-10 version (Note 9)
<i>Signal applied with Rx on</i>						
RSSI response time (rise/fall)	11	-	100	-	μ s	
Signal to stable data, $t_{sig-data}$	12	-	0.3	2	ms	-64 version (Note 9)
Signal to stable data, $t_{sig-data}$	12	-	1	3	ms	-10 version (Note 9)
Time between data transitions	14	15.6	-	1500	μ s	-64 version
Time between data transitions	14	0.1	-	10	ms	-10 version
Averaged code mark:space	14	40	50	60	%	Note 8

Note:

1. Increases at high RF input level (>-20dBm)
2. See page 6 for further details
3. For specified FM deviation
4. Tx select low > full RF output
5. \pm 30kHz FM deviation
6. Min/max After stabilisation
7. Max at \pm 50kHz offset
8. Average, at max. data rate
9. At max. data rate

Application Information

Modulation formats and range extension

The module will produce the specified FM deviation with a 2-level digital input to TXD which toggles between 0V and 3V. Reducing the amplitude of the data input from this value (usually as a result of reducing the supply voltage) reduces the transmitted FM deviation, typically to \square 20-22kHz minimum at 2.7V. The receiver will cope with this quite happily and no significant degradation of link performance should be observed.

TXD is normally driven directly by logic levels but will also accept analogue drive, e.g. 2-tone signalling. In this case it is recommended that TXD (pin 14) should be DC-biased to 1.5V with the modulation AC-coupled and limited to a maximum of 3V peak-to-peak. The instantaneous modulation voltage must not swing below 0V or above 3V at any time if waveform distortion and excessive FM deviation is to be avoided – use a resistive potential divider and/or level shifter to accomplish this if necessary. The varactor modulator in the transmitter introduces some 2nd harmonic distortion which may be reduced if necessary by predistortion of the analogue waveform.

At the other end of the link the AF output (pin 13) can be used to drive an external decoder directly.

Data formats containing a DC component are unsuitable and should not be used. The time constant of the peak detector data slicer in the BiM3C is set at a reasonable compromise to allow the use of low code speeds where necessary whilst keeping settling times acceptably fast for battery-economised operation. RXD output on pin 12 is “true” sense, i.e. as originally fed to the transmitter.

In applications such as longer range fixed links where data speed is not of primary importance, a significant increase in range can be obtained by using the slowest possible data rate together with filtering to reduce the receiver bandwidth to the minimum necessary. In these circumstances the AF output can be used to drive an external filter and data slicer. The AF output waveform on pin 13 is in the same sense as that originally fed to the transmitter, i.e. no inversion takes place but, it has a DC varying offset present at all times whilst the data slicer tracks any frequency errors.

Received Signal Strength Indicator (RSSI)

The BiM3C receiver incorporates a wide range RSSI which measures the strength of an incoming signal over a range of approximately 60dB. This allows assessment of link quality and available margin and is useful when performing range tests.

Please note that the actual RSSI voltage at any given RF input level varies somewhat between units. The RSSI facility is intended as a relative indicator only - it is not designed to be, or suitable as, an accurate and repeatable measure of absolute signal level or transmitter-receiver distance.

The output on pin 11 of the module has a standing DC bias in the region of 0.25V with no signal, rising to around 1.15V at maximum indication (RF input levels of -50dBm and above). For any given RF input level, absolute RSSI voltage is likely to vary somewhat between individual units - please refer to specifications table on p4.

The RSSI output source impedance is high (~50k) and external loading should therefore be kept to a minimum.

Typical RSSI characteristic is shown below (this is for indicative purposes only and is not a guarantee of actual RSSI characteristics):

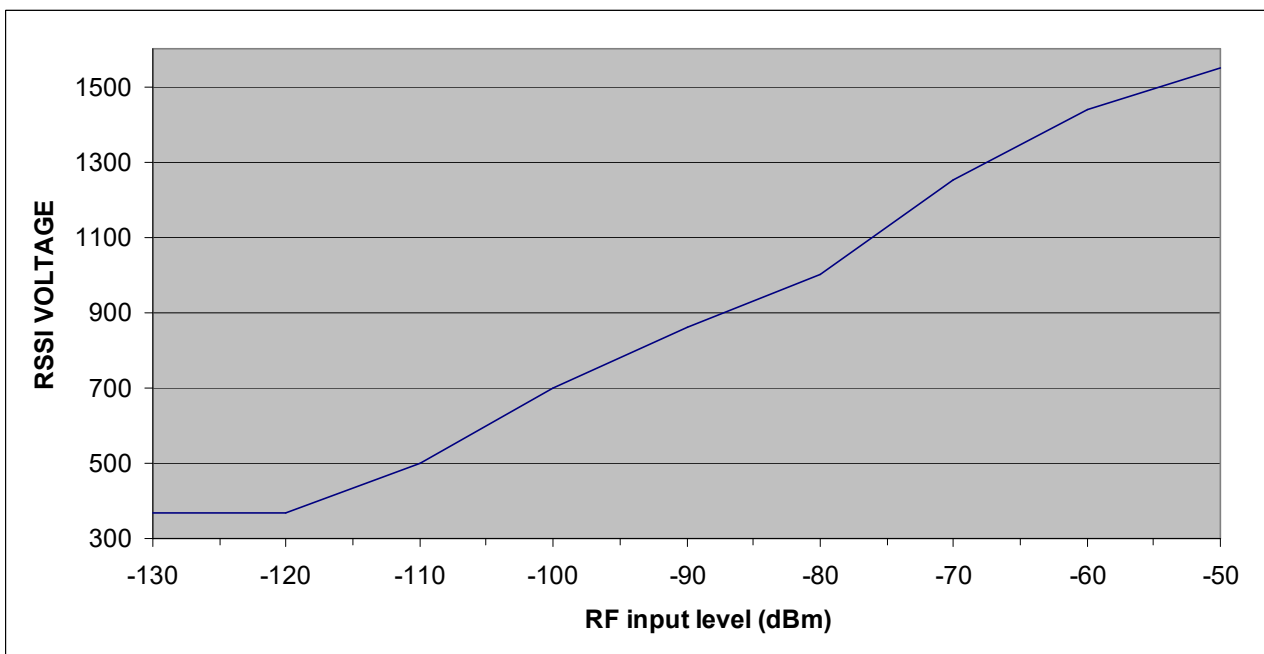


Fig.4: Typical RSSI response curve

To ensure a reasonably fast response the RSSI has limited internal decoupling of 1nF to ground. This may result in a small amount of audio ripple on the DC output at pin 11 of the module. If this is a problem further decoupling may be added at the expense of response speed, in the form of a capacitor from pin 11 to ground. For example, adding 10nF here will increase RSSI response time from 100µs to around 1ms. The value of this capacitor may be increased without limit.

Expected range

Predicting the range obtainable in any given situation is notoriously difficult since there are many factors involved. The main ones to consider are as follows:

- Type and location of antennas in use (see below)
- Type of terrain and degree of obstruction of the link path
- Sources of interference affecting the receiver
- “Dead” spots caused by signal reflections from nearby conductive objects
- Data rate and degree of filtering employed (see page 5)

Assuming the maximum 64kb/s data rate and $\frac{1}{4}$ -wave whip antennas on both transmitter (@ 5mW) and receiver, the following ranges may be used as a rough guide only:

Cluttered/obstructed environment, e.g. inside a building	:	50-75m
Open, relatively unobstructed environment	:	200-300m

It must be stressed, however, that range could be much greater or much less than these figures. Range tests should *always* be performed before assuming that a particular range can be achieved in any given application.

Antenna considerations and options

The choice and positioning of transmitter and receiver antennas is of the utmost importance and is the single most significant factor in determining system range. The following notes are intended to assist the user in choosing the most effective arrangement for a given application.

Nearby conducting objects such as a PCB or battery can cause detuning or screening of the antenna which severely reduces efficiency. Ideally the antenna should stick out from the top of the product and be entirely in the clear, however this is often not desirable for practical or ergonomic reasons and a compromise may need to be reached. If an internal antenna must be used, try to keep it away from other metal components and pay particular attention to the “hot” end (i.e. the far end), as this is generally the most susceptible to detuning. The space around the antenna is as important as the antenna itself.

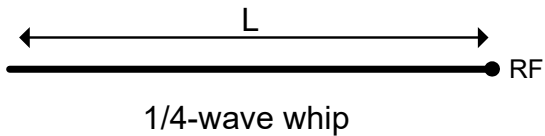
Microprocessors and microcontrollers tend to radiate significant amounts of radio frequency hash, which can cause desensitisation of the receiver if its antenna is in close proximity. 868MHz band is generally less prone to this effect than lower frequencies, but problems can still arise. Things become worse as logic speeds increase, because fast logic edges are capable of generating harmonics across the UHF range which are then radiated effectively by the PCB tracking. In extreme cases system range can be reduced by a factor of 3 or more. To minimise any adverse effects, situate the antenna and module as far as possible from any such circuitry and keep PCB track lengths to the minimum possible. A ground plane can be highly effective in cutting radiated interference and its use is strongly recommended.

A simple test for interference is to monitor the receiver RSSI output voltage, which should be the same regardless of whether the microcontroller or other logic circuitry is running or in reset.

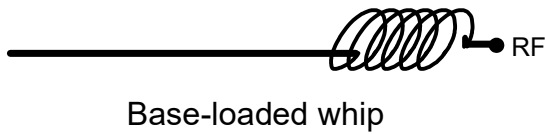
Two types of antenna are recommended for use with the BiM3B:

Whip ($\frac{1}{4}$ -wave). This consists simply of a piece of wire or rod connected to the module at one end. The lengths given below are from module pin to antenna tip including any interconnecting wire or tracking (but **not** including any 50 Ω coax or microstrip connection). This antenna is simple, cheap, easy to set up and performs well. It is especially effective when used with a ground plane, which in practice is often provided by the main PCB or by a metal case.

Base-loaded whip. In applications where space is at a premium a shortened whip may be used, tuned by means of a coil inserted at the base. This coil may be air-wound for maximum efficiency, or a small SMT inductor can be used. The value must be empirically chosen to tune the particular length of whip for best results “in situ”, making this antenna more difficult to set up. Radiated power will generally be slightly less than that obtained from a $\frac{1}{4}$ -wave whip.



wire, rod, PCB track
or a combination of these
 $L \text{ (mm)} = 71250 / \text{freq(MHz)}$



shortened wire, rod, PCB track etc.
with loading coil.
SMT inductor may be used if reqd.

Fig. 5: Antenna configurations

Other types of antennas are feasible but tend to have drawbacks at these frequencies. Helical and tuned loop antennas are both very compact but tricky to set up, and can be impractical at 868MHz because of their very small size. Microstrip patch antennas are relatively large in area, directional, and have gain.

Note: Where the specified antennas are mounted on the PCB and/or in close proximity to metalwork (module casing, components, PCB tracking etc), the antenna radiation pattern may be seriously affected. Radiated power may be significantly increased in some directions (sometimes by as much as 10dB) and correspondingly reduced in others. This may adversely affect system performance where good all-round coverage is desired.

Care should also be taken to ensure that this effect does not increase the radiated power in any direction beyond that allowed by type approval regulations. Where this occurs the antenna may need to be relocated. In extreme cases a resistive attenuator of appropriate value may be required between the module and antenna.

Type Approval requirements: Europe

The modules comply with the requirements of the R&TTE Directive (including standards EN 300 220-3 and EN 301 489-3) when used in accordance with the information contained herein. The following provisos apply:

- 1) The modules must not be modified or used outside their specification limits.
- 2) The modules may only be used to transfer digital or digitised data. Analogue speech and/or music are not permitted.
- 3) The equipment in which the BiM3C is used must carry all necessary external labelling to meet the requirements of the R&TTE directive.
- 4) The BiM3C has not been tested with antennas having gains greater than that of a quarter-wave whip. The use of such antennas may require further R&TTE approval.

Module mounting considerations

Good RF layout practice should be observed. In particular, any ground return required by the antenna or feed should be connected directly to the RF GND pins at the antenna end of the module, and not to the GND pins which are intended as DC grounds only. All connecting tracks should be kept as short as possible to avoid any problems with stray RF pickup.

If the connection between module and antenna does not form part of the antenna itself, it should be made using 50Ω microstrip line or coax or a combination of both. It is desirable (but not essential) to fill all unused PCB area around the module with ground plane.

Variants and ordering information

BiM3C modules are manufactured in the following variants as standard:

For European applications in the 868 - 870MHz SRD band:

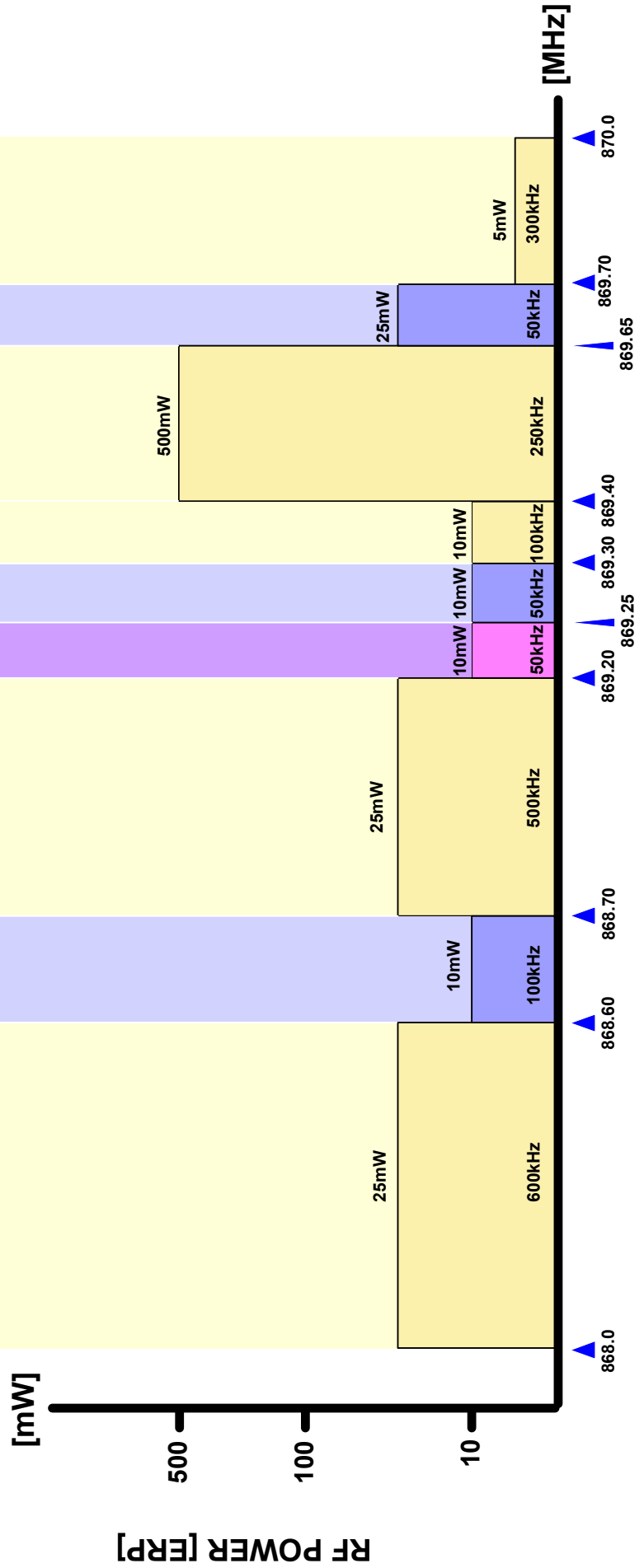
For US, Canadian application in the 902-928MHz ISM band:

Part number	RF power (typ.)	Frequency	Data rate
BiM3C-869.85-64	5mW	869.85MHz	64kbps
BiM3C-869.85-10	5mW	869.85MHz	10kbps
BiM3C-868.30-64	25mW	868.30MHz	64kbps
BiM3C-868.30-10	25mW	868.30MHz	10kbps

Non-standard versions can be supplied to individual customer requirements, optimised for specific data speeds/formats. Minimum order quantities apply. Please consult the Sales department for further information.

CEPT/ERC Rec 70-03, 868 MHz Band Plan

Applications	Non-specific SRD	Alarm	Non-specific SRD	Social Alarm	Alarm	Non-specific SRD	Alarm	Non-specific SRD	Alarm	Non-specific SRD
Channel Spacing	No Spacing	25kHz or WB	No Spacing	25kHz	25kHz	25kHz or Wide Band	25kHz	25kHz	25kHz	No Spacing
Duty Cycle	<1% or LBT	<1%	<0.1% or LBT	<0.1%	<0.1%	<10% or LBT	<10%	<1%	<10%	up to 100%



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The Intrastat commodity code for all our modules is: 8542 6000

R&TTE Directive

After 7 April 2001 the manufacturer can only place finished product on the market under the provisions of the R&TTE Directive. Equipment within the scope of the R&TTE Directive may demonstrate compliance to the essential requirements specified in Article 3 of the Directive, as appropriate to the particular equipment.

Further details are available on The Office of Communications (Ofcom) web site:

<http://www.ofcom.org.uk/>

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